

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in this application.

Listing of the Claims:

Claims 1 and 2 (Cancelled)

3. (Currently Amended) An add-on card for detachably coupling to a processing system comprising:

 an interface for communicating with said processing system while said add-on card is coupled with said processing system;

 a program storage memory storing at least one operating sequence;

 a mass storage memory including a portion for storing user data and a program memory portion storing at least two additional operating sequences;

 a processing unit coupled to said interface, said program storage memory, and said mass storage memory, whereby the processing unit can operate on user data transferred between the mass storage memory and the processing system through the interface according to an operating sequence selected by the processing system from said at least two additional operating sequences;

 a card bus to which the processing unit, the interface and the program storage memory are connected; and

 a mass storage interface by which the mass storage memory is connected to the card bus, wherein the mass storage interface is a non-linear interface;

wherein the mass storage memory stores data in a non-linear structure, wherein

the data is only accessible in a block that is too large for the processing unit to utilize without first reading out and caching the block, and wherein the mass storage interface is operative to retrieve the data from the mass storage memory and store the data in a linear form usable by the processing unit.

4. (Previously Presented) The add-on card of claim 3, wherein the data transferred between the card and the processing system is continuous media.

5. (Original) The add-on card of claim 4 further comprising:
a data cache memory connected to the processor and the mass storage memory for buffering the continuous media transferred between the card and the processing system.

6. (Previously Presented) The add-on card of claim 3, wherein said at least two additional operating sequences includes a decompression program.

7. (Previously Presented) The add-on card of claim 3, wherein said at least two additional operating sequences includes a compression program.

8 (Previously Presented) The add-on card of claim 3, wherein at least two additional operating sequences includes a data encryption/decryption routine.

9. (Previously Presented) The add-on card of claim 3, wherein at least two additional operating sequences includes a voice recognition routine.

10. (Previously Presented) The add-on card of claim 3, wherein the mass storage memory is a FLASH memory.

11. (Previously Presented) The add-on card of claim 3, wherein the mass storage memory further includes a portion storing system data, whereby the processing unit can operate on data transferred between the card and the processing system using the system data.

Claims 12-14 (Cancelled)

15. (Currently Amended) An add-on card for detachably coupling to a processing system comprising:

- an interface for communicating with said processing system while said add-on card is coupled with said processing system;

- a program storage memory storing an operating sequence;

- a processing unit coupled to said interface and said program storage memory;

- a mass storage memory including a portion for storing user data coupled to said processing unit, whereby the processing unit operates on user data transferred between the interface and the portion of the mass storage memory for storing user data according to said operating sequence when said operating sequence is enabled by said processing system but does not operate on the user data when said operating sequence is not enabled by said processing system;

- a card bus to which the processing unit, the interface and the program storage

memory are connected; and

a mass storage interface by which the mass storage memory is connected to the card bus, wherein the mass storage interface is a non-linear interface;

wherein the mass storage memory stores data in a non-linear structure, wherein the data is only accessible in a block that is too large for the processing unit to utilize without first reading out and caching the block, and wherein the mass storage interface is operative to retrieve the data from the mass storage memory and store the data in a linear form usable by the processing unit.

16. (Previously Presented) The add-on card of claim 15, wherein the mass storage memory includes a program memory portion storing at least one additional operating sequence.

17. (Previously Presented) The add-on card of claim 15 wherein the data transferred between the interface and the mass storage memory is continuous media.

18. (Original) The add-on card of claim 17, further comprising:

a data cache memory connected to the processor and the mass storage memory for buffering the data transferred between the interface and the mass storage memory, wherein the data transferred is stored non-linearly.

19. (Previously Presented) The add-on card of claim 17, wherein at least a portion of the mass storage memory contains continuous media prerecorded by the card supplier.

20. (Previously Presented) The add-on card of claim 17, wherein said operating sequence is a decompression program.

21. (Previously Presented) The add-on card of claim 17, wherein said operating sequence is a compression program.

22. (Previously Presented) The add-on card of claim 15, wherein the data transferred between the interface and the mass storage memory is a navigation data base.

23. (Previously Presented) The add-on card of claim 15, wherein said operating sequence is a data encryption/decryption routine.

24. (Previously Presented) The add-on card of claim 15, wherein said operating sequence is a voice recognition routine.

25. (Previously Presented) The add-on card of claim 15, wherein the mass storage memory is a FLASH memory.

Claims 26-64 (Cancelled)

65. (Previously Presented) The add-on card of claim 3, wherein said processing unit selectively operates in combination with a processor of said processing system on said user data received from the processing system.

Claim 66 (Cancelled)